IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent No. **7,050,343**   )
KUMAR ET AL.   )
) Attorney Docket No. **53406**
Issued: **MAY 23, 2006**   )
)
For: **BUILT-IN TESTING METHODOLOGY**   )
IN FLASH MEMORY   )
)
_____   )

REQUEST FOR CORRECTED CERTIFICATE OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

        The Original Certificate of Correction has been received and reviewed for accuracy. The Original Certificate of Correction is incorrect because Column 14, Line 45; Column 16, Line 26 and Column 16, Line 42 were not corrected. However, Column 14, Line 45; Column 16, Line 26 and Column 16, Line 4 are correct in the Certificate of Correction Form PTO-1050 that the Patentee provided to the Patent Office with its request of September 21, 2006.

        Please find a Certificate of Correction Form PTO-1050 with the correct changes attached hereto. Copies of both the Original Certificate of Correction and the Form PTO-1050 sent with Patentee's original request are enclosed. Since these errors is believed to be the error of the Patent Office, the new Certificate of Correction should be printed at no expense to the Patentee. No new matter is added by this correction.

        If any other fee is required, please charge the deposit account number 01-0484.

In re Patent of
KUMAR ET AL.
Patent No. 7,050,343
Issued: **MAY 23, 2006**

JAN 2 2 2007

Respectfully submitted,

Christopher F. Regan
Reg. No. 34,906
Allen, Dyer, Doppelt, Milbrath
    & Gilchrist, P.A.
255 S. Orange Ave., Suite 1401
P. O. Box 3791
Orlando, Florida 32802
(407) 841-2330

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being
deposited with the United States Postal Service as first class
mail in an envelope addressed to: Commissioner for Patents,
P.O. Box 1450, Alexandria, VA 22313-1450, on this _17th_ day of
January, 2007.

JAN 2 2 2007

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO. :	US 7,050,343 B2
DATED :	MAY 23, 2006
INVENTOR(S) :	KUMAR ET AL.

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below

Column 14, Line 45	Delete: "vgmax or vgmin" Insert --Vgmax or Vgmin--
Column 16, Line 26	Delete: "ck, ckn, all0/all1" Insert --CK, CKN, All0/All1--
Column 16, Line 42	Delete: "all0/all1" Insert --All0/All1--

MAILING ADDRESS OF SENDER:
Christopher F. Regan
Allen, Dyer, Doppelt, Milbrath & Gilchrist, P.A.
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P.O. Box 3791
Orlando, FL 32802-3791
PHONE : (407) 841-2330

PATENT NO. __US 7,050,343 B2__

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO.   : 7,050,343 B2
APPLICATION NO. : 10/789443
DATED   : May 23, 2006
INVENTOR(S)   : Promod Kumar et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1, Line 67	Delete: "failures" Insert: -- failure --
Column 2, Line 32	Delete: "to the" Insert: -- on the --
Column 4, Line 59	Delete: "relatives" Insert: -- relative --
Column 4, Line 62	Delete: "register" Insert: -- registers --
Column 4, Lines 66-67	Delete: "in which preserve" Insert: -- which preserves --
Column 5, Line 50	Delete: "at the time" Insert: -- at a time --
Column 7, Line 14	Delete: "contains" Insert: -- contained --
Column 7, Line 16	Delete: "occurs" Insert: -- occurred --
Column 7, Line 44	Delete: "of the all" Insert: -- of all --
Column 7, Line 55	Delete: "are" Insert: -- is --
Column 7, Line 62	Delete: "identify" Insert: -- identifying --
Column 8, Line 12	Delete: "occupy" Insert: -- occupying --

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,050,343 B2
APPLICATION NO. : 10/789443
DATED : May 23, 2006
INVENTOR(S) : Promod Kumar et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 8, Line 21	Delete: "two side" Insert: -- two sides --
Column 9, Line 18	Delete: "to the" Insert: -- than the --
Column 9, Line 39	Delete: "column" Insert: -- columns --
Column 10, Line 14	Delete: "erase" Insert: -- Erase --
Column 10, Line 15	Delete: "read" Insert: -- Read --
Column 10, Line 20	Delete: "setting" Insert: -- set --
Column 11, Line 13	Delete: "at beginning" Insert: -- at the beginning --
Column 11, Line 15	Delete "electronic" Insert: -- Electronic --
Column 11, Line 22	Delete: "at the time" Insert: -- at a time --
Column 11, Lines 33-34	Delete: "parametric measurement" Insert: -- Parametric Measurement --
Column 12, Line 34	Delete: "erase the step" Insert: -- the erase step --
Column 12, Line 60	Delete: "perform" Insert: -- performs --
Column 13, Line 18	Delete: "correspond" Insert: -- corresponds --

UNITED STATES PATENT AND TRADEMARK OFFICE
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PATENT NO.   : 7,050,343 B2
APPLICATION NO. : 10/789443
DATED   : May 23, 2006
INVENTOR(S)   : Promod Kumar et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 13, Line 22	Delete: "catch" Insert: -- Cache --
Column 13, Line 64	Delete: "vgmin/vgmax" Insert: --Vgmin/Vgmax--
Column 14, Line 2	Delete: "vgmax/vgmin" Insert: -- Vgmax/Vgmin --
Column 14, Line 26	Delete: "vgmax/vgmin" Insert: -- Vgmax/Vgmin --
Column 14, Line 28	Delete: "vgmax/vgmin" Insert: -- Vgmax/Vgmin --
Column 14, Line 30	Delete: "for vgmax" Insert -- for Vgmax --
Column 14, Line 31	Delete: "for vgmin" Insert: --for Vgmin --
Column 14, Line 45	Delete: "vgmax or vgmin" Insert: -- ~~vgmax or Vgmin~~ -- -- Vgmax or Vgmin --
Column 14, Lines 65 and 66	Delete: "vgmax" Insert: -- Vgmax -- Delete: "vgmin" Insert: -- Vgmin --
Column 15, Line 16	Delete: " all0/all1" Insert: -- All0/All1 --
Column 16, Line (second column)	Delete: "standard" Insert: -- invention --
Column 16, Line 26	Delete: "ck, ckn, all0/all1" Insert: -- ~~CK, CKN, All0/All1~~ -- -- CK, CKN, All0/All1 --
Column 16, Line 28	Delete: "ckn" Insert: -- CKN --

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

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DATED   : May 23, 2006
INVENTOR(S)   : Promod Kumar et al.

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Column 16, Lines 29 and 30
Delete: "ck"
Insert: -- CK --

Column 16, Line 33
Delete: "Ck/Ckn"
Insert: -- CK/CKN --

Column 16, Line 42
Delete: "all0/all1"
Insert: --All0/All1 -- -- All0/All1

Column 16, Line 60
Delete: "both"
Insert: -- by--

Column 17, Line 13
Delete: "numerosity"
Insert: -- number--

Column 17, Line 23
Delete: "an built-in"
Insert: -- a built-in --

Column 17, Line 42
Delete: "of the time"
Insert: -- as the time --

Column 19, Line 47
Column 20, Line 46
Delete: "microcontroller"
Insert: -- micro-controller --

Signed and Sealed this

Fifth Day of December, 2006

JON W. DUDAS
Director of the United States Patent and Trademark Office

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent No. **US 7,050,343 B2**)
KUMAR ET AL.   )
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Issued: **MAY 23, 2006**   )
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For: **BUILT-IN TESTING METHODOLOGY**   )
    IN FLASH MEMORY   )
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REQUEST FOR CERTIFICATE OF CORRECTION OF APPLICANT'S MISTAKE UNDER RULE 1.323

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of Rule 1.323 of the Rules of Practice, the Patent Office is respectfully requested to correct matters in the above-identified patent in accordance with the Certificate of Correction Form PTO-1050 attached hereto.

The Patent Office is requested to issue a Certificate of Correction on the above-referenced Patent Document, and to place such a Certificate of Correction in the file, so that such will appear on any copies of the patent which are ordered in the future.

The corrections being made are of consequence to provide the proper recitation in the specification and claims. The mistakes occurred in good faith and are not the fault of the Patent and Trademark Office. However, one printing error of the Patent Office is also being corrected. No new matter is added by these corrections.

A check for the requisite fee as set forth in 37 CFR

In re Patent of
KUMAR ET AL.
Patent No. **US 7,050,343 B2**
Issued: **MAY 23, 2006**

_____/

COPY

§ 1.20(a) in the amount of $100.00 is included herewith. The
Commissioner is authorized to charge or credit any
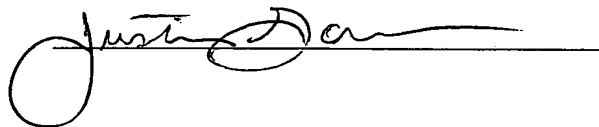discrepancies in fee amounts to Deposit Account 01-0484.

Respectfully submitted,

Christopher F. Regan
Reg. No. 34,906
Allen, Dyer, Doppelt, Milbrath
    & Gilchrist, P.A.
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Orlando, Florida 32802
(407) 841-2330

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being
deposited with the United States Postal Service as first class
mail in an envelope addressed to: COMMISSIONER FOR PATENTS,
P.O. BOX 1450, ALEXANDRIA, VA 22313-1450 on this _21st_ day of
September, 2006.

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PATENT NO. US 7,050,343 B2

<table>
<tr><td colspan="2" align="center">UNITED STATES PATENT AND TRADEMARK OFFICE</td></tr>
</table>

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Column 19, Line 47 Column 20, Line 46	Delete: "microcontroller" Insert: -- micro-controller --

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